	Q.P.	coae:	10EC410	
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Q.P.	. C	oae: 10	6EC41	0											
Reg	5.	No:											]		
		SIDD	HART	H INS	TITU	J <b>TE C</b>	F EN	GINE	ERIN	G &	TECH	INOL	_ LOGY:: PUT	TUR	
							(AU	TON	OMOL	JS)					
		B.Te	ch II \	ear II	Sem	ester	Sup	pleme	entary	/ Exa	mina	tions	February-2	.022	
					(171	PULS	E & I	DIGII	AL C		UITS				
Time		2 1			(Elect	tronics	s and	Comm	unicat	ion Ei	nginee	ering)		1 60	
Time	3	5 nours											Max. Mai	:ks: 60	
					(An	iswer a	all Fiv	e Unit UNI	s 5 x 1 T-I	12 = 6	0 Mar	ks)			
1	a	Desig	n high	pass R	C circ	cuit for	r sinus	soidal	input.					61	M
	b	Prove	that a	low pa	ss circ	cuit ac	ts as a	in integ	grator.					61	М
								O	R						
2	a	State	and pro	ove cla	mping	g circu	it theo	orem.						61	M
	b	With clippe	the heler.	p of a	neat	circui	t diag	ram, e	xplain	the v	workir	ng of	a two-level c	liode 61	M
								UNIT	Г-П						
3	a	Elabo charac	rate a cteristic	bout j es.	piece-	wise	linear	appr	oxima	tion	for a	sem	iconductor c	liode 61	M
	b	Briefl	y expla	in the	design	n of tra	ansiste	or swit	ch.					6N	M
								OI	2						
4	D	iscuss a	about th	ne Sch	mitt tr	rigger	circui	t with	neat di	iagrar	n.			12	M
								UNIT	'-III						
5	B	riefly e	xplain	the wo	rking	of a tr	ansist	or boo	tstrap	time	base g	enerat	tor.	12	М
					0			OI	8						
6	a	Expla	in the v	vorkin	g of T	ransis	tor Mi	iller sv	veep c	ircuit.				6N	M
	b	What	is its a	lvanta	ges mi	iller o	ver Bo	otstra	p swee	ep circ	cuits?			6M	M
								UNIT	-IV	-					
7	a	Give a	a brief	review	about	appli	cation	s of th	e sam	oling	gate?			61	м
	b	Explai	in abou	t unidi	rectio	nal di	ode sa	mplin	g gate.	, ing	Bare.			61	M
								O	8					UT.	
8	a	Discus	ss the f	unction	n of a	sampl	ing ga	ate use	d in Sa	ampli	ng Sco	opes.		61	M
	b	Draw	and ex	plain th	ne red	uction	ofpe	destal	in a ga	ate cir	cuit	1 .		6N	M
								UNIT	<b>-V</b>						
9	a	Explai	in the s	vnchro	nizati	on of	sweer	circu	it with	symr	netric	siona	s	61	Л
	b	How a	a sine v	ave fr	eauen	cy div	ision	is done	with	a swe	en cir	cuit?	15.	61	M
					- 1	-j urv	101011		2	abric	op on	ourt:		UI	VI.
10	W	ith refe	erence	o logi	c gates	s exnla	ain the	e terms						12	М
	(i)	Fan ou	ut (ii	) Nois	e marg	gin	(iii) F	ropaga	ation c	lelay	(i	v) Fig	gure of Merit	14)	A V A

\*\*\* END \*\*\*